

| Ref # | Hits | Search Query                                                                                               | DBs                                                               | Default Operator | Plurals | Time Stamp       |
|-------|------|------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------|------------------|---------|------------------|
| L1    | 227  | duty adj cycle adj correction and (dll or delay adj lock\$3 adj loop or pll)                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/26 14:49 |
| L2    | 4153 | (327/170-175).CCLS.                                                                                        | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/11/26 14:47 |
| L3    | 42   | (dll or delay adj lock\$3) and duty adj cycle adj correction with control\$4                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/26 14:48 |
| L4    | 51   | I1 and I2                                                                                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/26 14:48 |
| L5    | 440  | duty adj cycle adj correction                                                                              | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/26 14:48 |
| L6    | 105  | I5 and I2                                                                                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/26 14:48 |
| L7    | 970  | duty adj cycle with control\$4 and (dll or delay adj lock\$3 adj loop or pll)                              | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/26 14:49 |
| L8    | 1272 | duty adj cycle with (control\$4 or correct\$3 or adjust\$4) and (dll or delay adj lock\$3 adj loop or pll) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/26 14:49 |
| L9    | 100  | I2 and I8                                                                                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/26 14:50 |